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(FILE 'USPAT' ENTERED AT 11:49:17 ON 15 AUG 1999)
L1      23426 SEA (VIDEO(2A)PROCESS###) OR DSP
L2      0 SEA L1 AND (WAPER CARD#)
L3      0 SEA WAPER CARD#
L4      128 SEA WAPER
L5      14 SEA L1 AND (BILINEAR INTERPOLATOR)
L6      6 SEA L5 AND (PLA OR PAL OR PROGRAMMABLE LOGIC ARRAY OR PROG
RAM      MABLE ARRAY LOGIC)
L7      22363 SEA (PLURAL OR MULTIPLE OR DUAL) (2A) (MICROPROCESSOR OR COM
PUT      ER OR PROCESS###)
L8      8136 SEA (SHARED OR COMBINED OR PARTITION###) (2A) (MEMORY OR CAC
HE      OR RAM)
L9      2222 SEA L7 AND L8
L10     58040 SEA (CONCURRENT OR SIMULTANEOUS OR COINCIDENT OR INSTANTAN
EOU      S OR PARALLEL) (3A) (PROCESS### OR ACCESS### OR TASK### OR O
PER      AT###)
L11     1318 SEA L9 (P) L10
L12     1318 SEA L10 AND L9
L13     6317 SEA (CONCURRENT OR SIMULTANEOUS) (2A) (PROCESS### OR ACCESS
###      OR TASK###)
L14     2512 SEA (CONCURRENT OR SIMULTANEOUS) (2A) (ACCESS###)
L15     8501 SEA L13 OR L14
L16     539 SEA L15 AND L9
        SAVE ALL VIDPROCESS2/L
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FILE USPAT

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* * * * *
*   U. S.   P A T E N T   T E X T   F I L E   *
*
*   THE WEEKLY PATENT TEXT AND IMAGE DATA IS CURRENT   *
*   THROUGH AUGUST 10,1999                               *
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